

**PRASAD V. POTLURI SIDDHARTHA**  
**INSTITUTE OF TECHNOLOGY**  
**(AUTONOMOUS)**

**DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING**

***WORKSHOPS & SEMINARS (2020 – 2021)***

**Department of ECE**  
**Workshops & Seminars**

1. Webinar On Design Thinking & Product Innovation

The poster is for a webinar titled "Design Thinking & Product Innovation" organized by the Department of ECE at PVP Siddhartha Institute of Technology. It is part of the IETE Student Forum. The event is scheduled for July 18th, from 10 AM to 11 AM. The speaker is Dr. Srinivasa Murthy Boorla, General Manager of the Engineering Design Centre at Grupo Antolin India Pvt. Ltd., Vijayawada. The poster includes logos for PVP Siddhartha Institute of Technology and IETE (The Institution of Electronics and Telecommunication Engineers). A large lightbulb graphic is in the background. A clock icon shows the time 10:10, and a calendar icon shows July 18th. A speech bubble says "For II Year Students". A large orange button says "REGISTER NOW". At the bottom, there are boxes for the Coordinator (Dr. T. Haritha, Assoc. Prof., Dept. of ECE) and the Convener (Dr. P. Rajesh Kumar, Prof. & Head, Dept. of ECE).

**P V P Siddhartha Institute of Technology**

**DEPARTMENT OF ECE**

One Day Webinar  
on  
Design Thinking & Product Innovation  
under  
**IETE STUDENT FORUM**

**10 AM – 11 AM**

**JULY 18<sup>th</sup>**

**Dr. SRINIVASA MURTHY BOORLA,**  
GENERAL MANAGER,  
ENGINEERING DESIGN CENTRE,  
Grupo antolin India Pvt. Ltd., VIJAYAWADA

**REGISTER NOW**

**For II Year Students**

**COORDINATOR**  
Dr. T. HARITHA,  
ASSOC. PROF., DEPT. OF ECE

**CONVENER**  
Dr. P. RAJESH KUMAR,  
PROF. & HEAD, DEPT. OF ECE

2. webinar on Introduction to Machine Learning

The poster is for a webinar titled "Introduction to Machine Learning" under the IETE Student Forum. It features the logos of P V P Siddhartha Institute of Technology and IETE at the top. The main text is in large, bold, yellow and white fonts. A central image shows a portrait of Dr. C S Sastry, Professor at IIT Hyderabad. To the left of the portrait is a clock icon indicating the time 10 AM - 11 AM. To the right is a calendar icon indicating the date JULY 27th. Below the portrait is a box with the speaker's name and affiliation. A large blue button with white text says "REGISTER NOW". A speech bubble on the right says "for II Year Students". At the bottom, two boxes list the coordinator and convener.

**P V P Siddhartha  
Institute of  
Technology**

**DEPARTMENT OF ECE**  
One Day Webinar  
on  
**Introduction to  
Machine Learning**  
under **IETE Student Forum**

**10 AM – 11 AM**

**JULY 27<sup>th</sup>**

**Speaker: Dr. C S Sastry,  
Professor, IIT Hyderabad**

**REGISTER NOW**

**for II Year  
Students**

**COORDINATOR  
Dr. T. HARITHA, ASSOC. PROF.**

**CONVENER  
Dr. P. RAJESH KUMAR, PROF. & HEAD**



3. Webinar on Evolution of Phased Arrays for Radars

**P V P Siddhartha Institute  
of Technology**

**DEPARTMENT OF ECE**  
A Webinar  
on  
**Evolution of Phased Arrays for Radars**  
Under IEEE Student Chapter

**10.30 AM – 12 AM**

**JULY 29<sup>th</sup>**

**Speaker: Dr. M. Lakshminarayana**  
Scientist-H (Retd.), DURL/DRDO  
Director R&D, Unistring Tech Solutions Pvt. Ltd.

**REGISTER NOW**

<b>COORDINATOR</b> Dr. C SUBBARAO, PROFESSOR	<b>CONVENER</b> Dr. P. RAJESH KUMAR, PROF. & HEAD
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#### 4. Python Programming

*PRASAD V POTLURI SIDDHARTHA INSTITUTE OF TECHNOLOGY*  
*KANURU, VIJAYAWADA – 7*  
*DEPARTMENT OF ECE.*

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### **Circular**

Dt. 01.08.2020

This is to inform that the Department of Electronics & Communication Engineering in association with Andhra Pradesh State Skill Development Corporation is conducting workshop on “**Python Programming**” for registered students of III B.Tech. ECE from 03.08.2020 to 21.08.2020.

**Resource Persons:**

Ambika Puvvula, Trainer APSSDC  
K Veeravanitha, Trainer APSSDC

**Venue:** Virtual class (GoToMeet)

**Time:** 2.00PM to 5.00PM

**Program Coordinator:**

Haji Md Habibulla

  
HOD- ECE

5. Webinar on Scope of DFT on SOC Design

The poster is for a webinar titled "Scope of DFT in SoC Design" under the IETE Student Forum. It features the logos of P V P Siddhartha Institute of Technology and IETE. The text includes "DEPARTMENT OF ECE", "Webinar on", "Scope of DFT in SoC Design", and "under IETE Student Forum". The time is listed as "02 PM – 03 PM" with a clock icon, and the date as "AUG 08<sup>th</sup>" with a calendar icon. The speaker is "Mr. L.B.K.Raju, Senior DFT Engineer, Bangalore". A prominent blue button says "REGISTER NOW". At the bottom, the coordinator is "Dr. T. HARITHA, ASSOC. PROF." and the convener is "Dr. P. RAJESH KUMAR, PROF. & HEAD".

**P V P Siddhartha  
Institute of  
Technology**

**DEPARTMENT OF ECE**

Webinar  
on  
**Scope of DFT in SoC Design**  
under IETE Student Forum

02 PM – 03 PM

AUG 08<sup>th</sup>

Speaker: Mr. L.B.K.Raju,  
Senior DFT Engineer,  
Bangalore

**REGISTER NOW**

**COORDINATOR**  
Dr. T. HARITHA, ASSOC. PROF.

**CONVENER**  
Dr. P. RAJESH KUMAR, PROF. & HEAD

6. MNC Expert talk on Emerging Technologies

**An MNC Expert Talk  
on  
Emerging Technologies**



**Speaker:**  
**Mr. Santosh Jaiswal**  
Assistant General Manager - Automation

**Join us through Google Meet**

10.00 AM – 11.30 AM



**AUG 28<sup>th</sup>**



**For III & IV Year Students of  
ECE & ME Engg. Departments  
First come first serve basis**

<b>COORDINATOR</b> Mrs. V. RATNA KUMARI, ASST. PROF.	<b>CONVENER</b> Dr. P. RAJESH KUMAR, PROF. & HEAD
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**DEPARTMENT OF ECE**  
**P V P Siddhartha Institute of Technology**



## 7. Online LabVIEW CORE 1&2 Training

Prasad V. Potluri Siddhartha Institute of Technology  
Department of Electronics and Communication Engineering

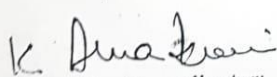
### CIRCULAR


Date: 01-08-2020

This is to inform that the Department of Electronics and Communication Engineering is conducting a training programme on NI LabVIEW core 1 & 2 from 03-08-2020 to 26-09-2020, with the details as under:

**Speaker:** Mohamedpharsath M  
Application Engineer, Opti thought, Chennai  
**Venue:** Virtual Class( Google Class room)  
**Time & Dates:** 4:00PM to 5:00PM, 3-08-2020 to 26-9-2020

**Note:** Selected Communication group Students of III E.C.E are required to attend without fail.

  
Faculty Co-ordinator:

  
(Dr.P.Rajesh Kumar)  
PROF. & H.O.D.




## 8. Online LabVIEW CLAD EXAM Training

**Prasad V. Potluri Siddhartha Institute of Technology**  
**Department of Electronics and Communication Engineering**

**CIRCULAR**

Date: 02-11-2020

It is hereby informed that the Department of Electronics and Communication Engineering is going to conduct NI LabVIEW certification program for the A.Y.2020-21 starting from November, 2020. All the interested III year students of ECE, EEE and ME Departments are requested to register their names in their respective departments. This year onwards there is no exam fee for appearing to CLAD exam. Faculty members can also attend for the CLAD examination to utilize NI software and hardware for their research work and their student project work.

  
HOD-ECE

CC to The Principal, PVPSIT

**HEAD**  
Electronics & Communication Engg. Dept.  
PRASAD V. POTLURI  
SIDDHARTHA INSTITUTE OF TECHNOLOGY  
KANURU, VILAYAWADA-520 002.

*Radha*

*MI-Saew*

## 9. Embedded System design using MSP430

PVP SIDDHARTHA INSTITUTE OF TECHNOLOGY  
KANURU, VIJAYAWADA - 7.

Dept. of E.C.E.

### GUEST LECTURES & SEMINARS COMMITTEE

Date: 27-12-2020

This is to inform all the concerned that an online training program on "Embedded System using MSP430" from 28-12-2020 to 31-12-2020 is being arranged by Guest Lectures & Seminars Committee of the Dept. with the details as under:


**Topic:** Online training program on Embedded system using MSP430

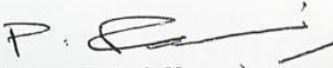
**Speaker:** Mr Nishanth  
Resource Person,  
EdGate Technologies,  
Bangalore

**Venue:** Virtual Class(Google Class room)

**Time & Dates:** 10:30AM to 5:00PM, 28-12-2020 to 31-12-2020 (8\_sessions) &  
Jan 7 & 8 th (Doubts clarification and Support)

Note: 1) ALL Students of IV E.C.E are required to attend without fail.

  
Faculty Co-ordinator,  
Guest Lectures & Seminars Committee.

  
(Dr.P.Rajesh Kumar)  
PROF. & H.O.D.

CC: 1. Notice Board.  
2. Principal for Information.

10. Design of Antennas for 5G applications using HFSS

PRASAD V POTLURI SIDDHARTHA INSTITUTE OF TECHNOLOGY,  
KANURU, VIJAYWADA – 520 007

Dt.24.12.2021.

DEPT. OF E.C.E.

CIRCULAR

It is hereby informed that a training program on **HFSS** will be conducted from 04/01/2021 to 12/01/2021 through online mode by Entuple Technologies, Bangalore. Interested students require to enrol their names with Dr. C. Subba Rao, Professor( Program Co-ordinator)

**HEAD**  
Electronics & Communication Engg. Dept.  
PRASAD V. POTLURI  
SIDDHARTHA INSTITUTE OF TECHNOLOGY  
KANURU, VIJAYWADA - 520 007.  
(Dr. P. Rajesh Kumar)  
Prof. & HOD., Dept. of ECE

Copy to:  
III Year Faculty members for intimation

## 11. FPGA based digital system design using Verilog

PVP SIDDHARTHA INSTITUTE OF TECHNOLOGY  
KANURU, VIJAYAWADA - 7.

Dept. of E.C.E.

### GUEST LECTURES & SEMINARS COMMITTEE

Date: 11-1-2021

This is to inform all the concerned that a training program on FPGA based digital system design using Verilog from 18-1-2021 to 28-1-2021 is being arranged by Guest Lectures & Seminars Committee of the Dept. with the details as under:

Topic: "Training program on FPGA based digital system design using Verilog"

Speaker: Mr K.Pradeep  
Application Engineer,  
ApplyVolt Technologies Pvt. Ltd,  
Hyderabad

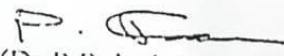
Venue: Virtual Class( Google Class room)

Time & Dates: 9:00 AM to 1.00 PM, 18-1-2021 to 28-1-2021 (8 days)

Note: 1) Selected Students of III E.C.E are required to attend without fail.

  
Faculty Co-ordinator,

Guest Lectures & Seminars Committee.

  
(Dr.P.Rajesh Kumar)  
PROF. & H.O.D.

CC: 1. Notice Board.  
2. Principal for Information.



12. webinar on Passion For Purpose Taking the New Year Resolution to the Next step

# A Webinar on Passion of Purpose – Taking The New Year Resolution To Next Step

**Speaker:**

**Sri Uma Maheswar Desu**

**Writer, Life Coach, Builder, Intelligence Pioneer, Publisher,  
Webmaster, Trekker, Innovator and Angel Investor.  
Managing Director of Intelli - India LLP**

**Join us through Google Meet**



**10.30 AM – 11.30 AM**



**20<sup>th</sup>  
Jan 2021**



**For III Year Students**

**COORDINATOR**

**Dr. T. HARITHA, ASSOC. PROF.**

**CONVENER**

**Dr. P. RAJESH KUMAR, PROF. & HEAD**



**Under IETE Student Forum  
DEPARTMENT OF ECE  
P V P Siddhartha Institute of Technology**



13. Digital system design using Verilog

PVP SIDDHARTHA INSTITUTE OF TECHNOLOGY  
KANURU, VIJAYAWADA – 7.

Dept. of E.C.E.

GUEST LECTURES & SEMINARS COMMITTEE

Date: 29-01-2021

This is to inform all the concerned that a guest lecture on “Digital System Design using Verilog” is being arranged by Guest Lectures & Seminars Committee of the Dept. with the details as under:

Topic: “Digital System Design using Verilog”

Speaker: Dr.N.Krishna Chaitanya  
Associate Professor,  
Sasi Institute of Technology & Engineering  
Tadepalligudem


Venue: E Class Room

Time & Date: 10.00 A.M, 30-01-2021


Note: Students of III E.C.E are required to attend without fail.

  
Faculty Co-ordinator

- CC: 1. Notice Board.  
2. Principal for Information.

  
(Dr.P.Rajesh Kumar)  
PROF. & H.O.D.  
HEAD

Assistant Professor, Communication Engg Dept.  
PRASAD V. POTLURI  
SIDDHARTHA INSTITUTE OF TECHNOLOGY  
KANURU, VIJAYAWADA-520 004.

  
PRINCIPAL  
PRASAD V. POTLURI  
SIDDHARTHA INSTITUTE OF TECHNOLOGY

14. Webinar on Personal Branding for Building Dream Career while Enjoying Student Life

**P V P Siddhartha  
Institute of Technology**

**DEPARTMENT OF ECE**

Webinar  
on  
**PERSONAL BRANDING FOR  
BUILDING DREAM CAREER  
WHILE ENJOYING  
STUDENT LIFE**  
under IETE Student Forum

**For II Year Students**

Join us through Google Meet

10.00 AM - 12.00 PM 02nd FEB 2021

**Speaker**  
**Prof. K V SUBBARAJU,**  
AUTHOR, MOTIVATIONAL TRAINER

**COORDINATOR**  
Dr. T. HARITHA, ASSOC. PROF.

**CONVENER**  
Dr. P. RAJESH KUMAR, PROF. & HEAD

15. Programming with Python

*PRASAD V POTLURI SIDDHARTHA INSTITUTE OF TECHNOLOGY*

*KANURU, VIJAYAWADA – 7*

*DEPARTMENT OF ECE.*

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**Circular**

Dt. 03.06.2021

This is to inform that the department of Electronics & Communication Engineering in association with Andhra Pradesh State Skill Development Corporation is conducting workshop on **“Python Programming”** for registered students of I B.Tech. ECE from 07.06.2021 to 26.06.2021.

**Resource Persons:**

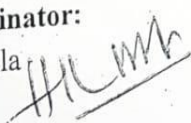
K Narmada Mani, Trainer APSSDC  
K.Mangatayaru, Trainer APSSDC

**Venue:** Virtual class (GoToMeet)

**Time:** 2.00PM to 4.00PM

**Program Coordinator:**

Haji Md Habibulla




  
HOD- ECE



16. Webinar on towards Ubiquitous Millimeter Wave Networks And Applications


**International Webinar**  
**on**  
**Towards Ubiquitous Millimeter-Wave Networks and Applications**





**Speaker**  
**Dr. Sanjib Sur, Asst.Prof,**  
**Computer Science & Engg.,**  
**University of South Carolina, USA**

**Join us through Google Meet**

**08.30 AM – 10.30 AM**  
**Indian time**




**AUG 26<sup>th</sup>**



**Faculty of ECE, CSE & IT Engg. Departments**  
**First come first serve basis**

<b>COORDINATOR</b> <b>Mr. M. SRINIVASA RAO ASSOC. PROF.</b>	<b>CONVENER</b> <b>Dr. P. RAJESH KUMAR, PROF. &amp; HEAD</b>
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**DEPARTMENT OF ECE**  
**P V P Siddhartha Institute of Technology**  
**Vijayawada, AP - 520007**

17. Webinar Labview for AI

**A Webinar  
on  
LabVIEW For  
Artificial Intelligence**



**Speaker**  
Ajay Kumar  
Founder & CEO,  
Cognibot Robotics

**Join us through Google Meet** 

**03.00 PM - 04.00 PM**  


**02nd Sep** 

**Staff of ECE Department**

**COORDINATOR**  
Dr. A. Geetha Devi,  
ASSOC. PROF.


**CONVENER**  
Dr. P. RAJESH KUMAR,  
PROF. & HEAD

**DEPARTMENT OF ECE**  
**P V P Siddhartha Institute of Technology**  
Vijayawada, AP - 520007




18. Webinar on writing a research paper with ease


A Webinar  
on  
**Writing a Research Paper  
with ease**




**Speaker**  
Dr. Narasimha Sarma NVS, Director,  
Indian Institute of Information Technology (IIIT),  
Tiruchirappalli

**Join us through Google Meet** 

02.00 PM - 03.30 PM




29<sup>th</sup> Sep 

**All branches Faculty Members**


<b>COORDINATOR</b> Dr. C. Subba Rao, PROFESSOR	<b>CONVENER</b> Dr. P. RAJESH KUMAR, PROF. & HEAD
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**DEPARTMENT OF ECE**  
**P V P Siddhartha Institute of Technology**  
Vijayawada - 520007, AP





## 19. Webinar Framing COs and mapping with POs



**PRASAD V POTLURI**  
**SIDDHARTHA INSTITUTE OF TECHNOLOGY**  
(AUTONOMOUS)  
KANURU, VIJAYAWADA - 520 007.  
(Sponsors : Siddhartha Academy of General & Technical Education, Vijayawada)  
**Affiliated to JNTUK, Kakinada (College Code : 50)**  
Approved by AICTE, Accredited by NAAC with 'A+' Grade  
UG Programs are Accredited by NBA  
An ISO 9001 : 2015 Certified Institution

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Ref :

Date :  
08-12-2020.


To  
Dr.P.V.D Soma Sekhar Rao  
Professor in ECE & Dean Academics  
G.Narayanaswami Institute of Technology and Science (Women)  
Shaikpet  
Hyderabad - 500008

Respected Sir,

Sub: Request to Deliver Invited Expert Talk – Reg.

We are in the process of preparing the syllabus for the PVP-2020 regulation of the B.Tech Course. Hence, we are interested to take your valuable suggestions for framing the COs and then mapping of COs to POs for the subjects related to the electromagnetic field. Your invited expert talk is scheduled for 11-12-2020 at 11.00 A.M @ Google Meet as per the discussion we had with you.

Thanking you,

Yours Sincerely,  
  
(Dr.P.Rajesh Kumar)  
Prof.& Head  
Dept. of ECE  
**HEAD**  
Electronics & Communication Engg Dept.  
PRASAD V. POTLURI  
SIDDHARTHA INSTITUTE OF TECHNOLOGY  
KANURU, VIJAYAWADA-520 007.

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Phones : (0866) 2581699, 2583037  
E-Mail : [principal@pvpsiddhartha.ac.in](mailto:principal@pvpsiddhartha.ac.in)

Fax : (0866) 2581194  
website : [www.pvpsiddhartha.ac.in](http://www.pvpsiddhartha.ac.in)



20. Three day training program on CRIO –FPGA

**A Three Day Training Program  
on  
CRIO-FPGA**

**Requisites: LabVIEW, NI myRIO or NI CRIO**

**Speaker**  
Senior application Engineer,  
Avgarde Systems Pvt. Ltd.,  
Bangalore.

**Join us through Google Meet** 

**18-01-2021 to 20-01-2021** 

**Faculty members &  
Research Scholars  
in the field of VLSI**

<b>COORDINATOR</b> Mr. B.SURYA PRASADA RAO, Assoc. Prof.	<b>CONVENER</b> Dr. P. RAJESH KUMAR, Prof. & Head
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**organized by**

**DEPARTMENT OF ECE**  
**P V P Siddhartha Institute of Technology**  
Vijayawada, AP - 520007

